

ABSTRACT OF THE DISCLOSURE

In an ultrasonic operation apparatus according to the present invention, in order to detect the initial resonance frequency (F_{ro}) promptly once a foot switch is pressed, under the control of a sweeping speed control circuit, the D/A converter D/A converts 8-bits of signals (phase difference amount signal $\Delta\theta$) representing the amount of the phase difference detected by the phase comparator, and the output of the D/A converter is output to a VCO so as to become a clock for a sweep circuit. When a phase difference amount between θ_i and θ_v is large and a drive frequency setting signal (F_s) is far from F_{ro} , the sweeping speed for F_{ro} detection is increased. When a phase difference amount between θ_i and θ_v is small and F_s is close to F_{ro} , the sweeping speed for F_{ro} detection is decreased.